



*PG Department of* **ELECTRONICS and  
INSTRUMENTATION TECHNOLOGY**  
University of Kashmir  
*Date sheet*

The Date Sheet for Examination of *M. Sc. Electronics 1<sup>st</sup> Semester (Regular) - Batch 2017* of this Department has been scheduled as under:

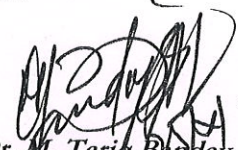
Venue: **Department of Electronics & Inst. Technology**

Time: **11:00 am**

Day and Date	Course No.	Course Title
6 <sup>th</sup> December, 2017 (Wednesday)	ELE-17101C	Circuit Analysis and Synthesis
9 <sup>th</sup> December, 2017 (Saturday)	ELE-17102C	Antennas and Wave Propagation
13 <sup>th</sup> December, 2017 (Wednesday)	ELE-17103C	Linear Integrated Circuits and Applications (LICA)
16 <sup>th</sup> December, 2017 (Saturday)	ELE-17104C	Digital Electronics and C-Programming Lab
20 <sup>th</sup> December, 2017 (Wednesday)	ELE-17105DCE	Engineering Mathematics
23 <sup>rd</sup> December, 2017 (Saturday)	ELE-17106DCE/ ELE-17107DCE	CMOS VLSI and Nano-Electronics –I (MOSFET Theory)/ Signals and Systems

Notes:

1. *All the concerned students are required to attend as per schedule.*
2. *The concerned Technical Staff/Faculty members are required to be available at 10:00 AM on the days of Examination*
3. *The Office is required to inform the concerned Laboratory Staff to arrange the Stationary for the Examination*
4. *Lab Examination for Courses having Laboratory Component shall be held on the same date.*

  
Dr. M. Tariq Bhat  
Head of the Department

No: F(DateSheet-MSc-2017)

Date: 25/11/2017

Copy to:

1. Departmental Website.
2. File.
3. Notice Board.