




PG Department of ELECTRONICS and INSTRUMENTATION TECHNOLOGY

University of Kashmir

Notice

In view of J&K bank examination for recruitment of Banking Associates and Probationary Officers on 23rd February 2019, the examination of the course papers titled “**CMOS VLSI and Nano Electronics – III (Analog and Mixed IC Design)**” and “**Advanced Communication Systems**” of M.Sc. 3rd Semester (Backlog) are rescheduled on 27th February, 2019. The timing and venue of the examination shall remain the same. All the concerned students are required to attend as per schedule.


Dr. M. Tariq Bhat
Head of the Department

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