

Mr. Azad-SB



PG Department of ELECTRONICS and
INSTRUMENTATION TECHNOLOGY
University of Kashmir

Date Sheet

(M. Sc. Electronics 2nd Semester (Regular))

(Ist Assessment)

The Date Sheet for Examination of M. Sc. (Electronics) 2nd Semester (Regular) Ist assessment of this Department has been scheduled as under:


Venue: Department of Electronics & Inst. Technology

Time: 11:00 am

Day and Date	Course No.	Course Title
7 th May, 2018 (Monday)	ELE-17202C	Microprocessor, Architecture, Interfacing and Programming
8 th May, 2018 (Tuesday)	ELE-17204C	Microwave Engineering
9 th May, 2018 (Wednesday)	ELE-17203C	Power Electronic Circuits and Systems
10 th May, 2018 (Thursday)	ELE17201C	Analog Communication Systems
11 th May, 2018 (Friday)	ELE17205DCE	VLSI Technology
14 th May, 2018 (Monday)	ELE17206DCE	CMOS VLSI and Nano-Electronics –II (Digital IC Design) Simulation and Modeling using MATLAB

Notes:

All the concerned students are required to attend as per schedule.


Dr. M. Tariq Bandy
Head of the Department

No: F(DateSheet-MSC-2018)

Date: 02/05/2018

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