



PG Department of **ELECTRONICS** and  
**INSTRUMENTATION TECHNOLOGY**

University of Kashmir

No: *Allexam/supp/msc*  
*electronics*  
Date: *8/7/2020*

**DATE SHEET FOR M.Sc (Electronics)**  
*1<sup>st</sup> to 4<sup>th</sup> semester (backlog) for the Batch-2017 (Supplementary Examination)*

Venue: Department of Electronics & IT  
Time: 11:00 AM

S.NO	DAY & DATE	COURSE NO	COURSE TITLE
1	Monday, 20 <sup>th</sup> July, 2020	ELE-17105 DCE	Engineering Mathematics
2	Thursday, 23 <sup>th</sup> July, 2020	ELE -17205 DCE	VLSI Technology
3.	Monday ,27 <sup>th</sup> July, 2020	ELE-17302CR/17303CR ELE-17405DCE	Control System / Engineering Digital Signal Processing / Computer Organization and Architecture

*(Signature)*  
**(Dr. M. Tariq Baiday)**  
Head of the Department

1. Departmental website
2. File