



PG Department of **ELECTRONICS and
INSTRUMENTATION TECHNOLOGY**
University of Kashmir

Revised Date Sheet (*M. Sc. Electronics level 2 (Backlog)*)

The Revised Date Sheet for Examination of *M. Sc. Electronics level 2 (Backlog)* of this Department has been scheduled as under:

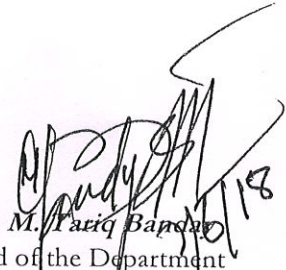
Venue: **Department of Electronics & Inst. Technology**

Time: **11:00 am**

Day and Date	Course No.	Course Title
21 st June, 2018 (Thursday)	ELE-15205DCE	VLSI TECHNOLOGY

Notes:

1. *All the concerned students are required to attend as per schedule.*


Dr. M. Fariq Bander
Head of the Department

No: F(DateSheet-MSc-2018)

Date: 07/05/2018

Copy to:

1. Web Publication.
2. File.
3. Notice Board