



PG Department of ELECTRONICS and  
INSTRUMENTATION TECHNOLOGY  
University of Kashmir

Date Sheet

(M. Sc. Electronics 2<sup>nd</sup> Semester (Regular))

(End Term)

The revised Date Sheet for Examination of M. Sc. (Electronics) 2<sup>nd</sup> Semester (Regular) 2<sup>nd</sup> assessment of this Department has been scheduled as under:

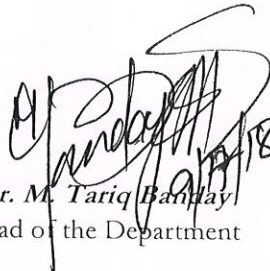
Venue: Department of Electronics & Inst. Technology

Time: 11:00 am

Day and Date	Course No.	Course Title
21 <sup>st</sup> July, 2018 (Saturday)	ELE17201C	Analog Communication Systems
24 <sup>th</sup> July, 2018 (Tuesday)	ELE-17203C	Power Electronic Circuits and Systems
26 <sup>th</sup> July, 2018 (Thursday)	ELE-17204C	Microwave Engineering
04 <sup>th</sup> August, 2018 (Saturday)	ELE17206DCE	CMOS VLSI and Nano-Electronics –II (Digital IC Design) Simulation and Modeling using MATLAB
07 <sup>th</sup> August, 2018 (Tuesday)	ELE-17202C	Microprocessor, Architecture, Interfacing and Programming
11 <sup>th</sup> August, 2018 (Saturday)	ELE17205DCE	VLSI Technology

Notes:

*All the concerned students are required to attend as per schedule.*

  
Dr. M. Tariq Baidar  
Head of the Department

No: F(DateSheet-MSC-2018)

Date: 09/07/2018

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