



PG Department of ELECTRONICS and INSTRUMENTATION TECHNOLOGY

University of Kashmir

Date Sheet (M. Sc. Electronics 3rd) (Backlog)

The Date Sheet for Examination of M. Sc. Electronics 3rd Semester (Backlog) of this Department has been scheduled as under:

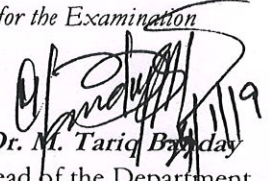
Venue: Department of Electronics & Inst. Technology

Time: 11:00 am

Day and Date	Course No.	Course Title
12 th February, 2019 (Tuesday)	ELE-15301C	Physics of Semiconductor Devices
14 February, 2019 (Thursday)	ELE-15302C	Control System Engineering
16 th February, 2019 (Saturday)	ELE-15303C	Digital Signal Processing
18 th February, 2019 (Monday)	ELE-15305DCE	Microcontrollers Architecture Interfacing and Programming
20 th February, 2019 (Wednesday)	ELE-15304C	Computer Networks
23 rd February, 2019 (Saturday)	ELE-15309DCE ELE-15306DCE	CMOS VLSI and Nano Electronics-III (Analog and Mixed IC Design) Advanced Communication Systems

Notes:

1. All the concerned students are required to attend as per schedule.
2. The concerned Technical Staff/Faculty members are required to be available at 10:00 AM on the days of Examination
3. The Office is required to inform the concerned Laboratory Staff to arrange the Stationary for the Examination


Dr. M. Tariq Baidar
Head of the Department

No: F(DateSheet-MSC-2019)

Date: 25/01/2019

Copy to:

1. Web Publication.
2. File.
3. Notice Board