



PG Department of **ELECTRONICS and
INSTRUMENTATION TECHNOLOGY**
University of Kashmir

Date Sheet (*M.Tech (Embedded Systems and Solutions) 2nd Semester*)

The Date Sheet for Examination of *M.Tech(Embedded Systems and Solutions) 2nd Semester* (Regular) of this Department has been scheduled as under:


Venue: **Department of Electronics & Inst. Technology**

Time: **11:00 am**

Day and Date	Course No.	Course Title
8 th November , 2017 (Wednesday)	ESS-205C	Embedded System Design with ARM Cortex Microcontrollers
13 th November , 2017 (Monday)	ESS-204C	CPLD and FPGA Architectures
17 th November , 2017 (Friday)	ESS-201R	Research Methodology
23 rd November, 2017 (Thursday)	ESS-206E4	Multimedia and Signal Coding
28 th November, 2017 (Tuesday)	ESS-203C	Embedded Programming and Languages

Notes:

1. *All the concerned students are required to attend as per schedule.*
2. *The dates of Lab examination and Seminar shall be notified separately.*


Dr. M. Tariq Banday
Head of the Department

No: F(DateSheet-M.Tech-2017)
Date: 20/10/2017

Copy to:

1. Web Publication.
2. File
3. Notice Board