



PG Department of **ELECTRONICS and
INSTRUMENTATION TECHNOLOGY**
University of Kashmir

Date Sheet (*M.Tech (Embedded Systems and Solutions) 2nd Semester*)

The Date Sheet for Laboratory course and seminar of *M.Tech (Embedded Systems and Solutions) (Batch-2018) 2nd Semester* (Regular) of this Department has been scheduled as under:

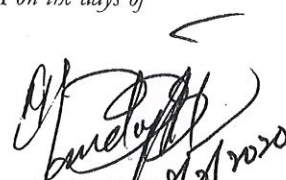
Venue: **Department of Electronics & Inst. Technology**

Time: **10:30 am**

Day and Date	Course No.	Course Title
12 th March, 2020 (Thursday)	ESS-207L	ARM Cortex Microcontroller Lab
14 th March, 2020 (Saturday)	ESS-208L	CPLD and FPGA Lab
19 th March, 2020 (Sunday) Thursday	ESS-202S	Seminar

Notes:

1. All the concerned students are required to attend as per schedule.
2. The concerned Technical Staff/Faculty members are required to be available at 10:00 AM on the days of Examination


Dr. M. Tariq Baidar
Head of the Department

No: F(DateSheet-M.Tech-2020)
Date: 28/02/2020

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1. Web Publication.
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