



PG Department of ELECTRONICS and
INSTRUMENTATION TECHNOLOGY
University of Kashmir

Date Sheet (M. Tech, 3rd Semester-Batch 2016)

The Laboratory Examination of M.Tech (Embedded Systems and Solutions 3rd Semester) Batch 2016 shall be held as per the following schedule:

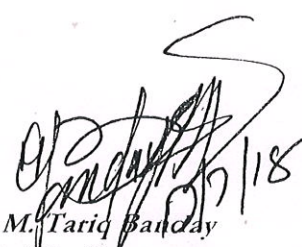
Venue: Department of Electronics & Inst. Technology

Time: 11:00 am

| Day and Date | Course No. | Course Title |
|---|------------|----------------------------------|
| 30 th July, 2018 (Monday) | ESS-306L | Mixed Signal and DSP Systems Lab |

Notes:

1. All the concerned students are required to attend as per schedule.


Dr. M. Tariq Baiday
Head of the Department

No: F(DateSheet-M.Tech(Sem-3)-2018)

Date: 17/07/2018

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2. Web Publication.
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