



*PG Department of ELECTRONICS and  
INSTRUMENTATION TECHNOLOGY*  
**University of Kashmir**

**Date Sheet (M.Tech (Embedded Systems and Solutions) 1<sup>st</sup> Semester)**

The Date Sheet for Examination of *M.Tech (Embedded Systems and Solutions) 1<sup>st</sup> Semester* (Regular and Backlog) of this Department has been scheduled as under:

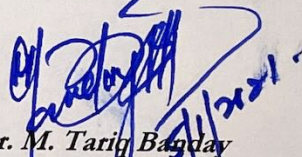
Venue: **Department of Electronics & Inst. Technology**

Time: **10:30 am**

Day and Date	Course No.	Course Title
18 <sup>th</sup> January, 2022 (Tuesday)	ESS-106C	Wireless Communication and Networks
21 <sup>st</sup> January, 2022 (Friday)	ESS-103C	Fundamentals of Embedded Systems
24 <sup>th</sup> January, 2022 (Monday)	ESS-105C	Advanced Digital System Design
28 <sup>th</sup> January, 2022 (Friday)	ESS-104C	Microcontrollers for Embedded System Design
31 <sup>st</sup> January, 2022 (Monday)	ESS-107L	Embedded System Design Lab
2 <sup>nd</sup> February, 2022 (Wednesday)	ESS-108L	Advanced Digital System Design and Wireless Communication lab

**Notes:**

1. All the concerned students are required to attend as per schedule.
2. The staff appointed for Examination duty and the examinees are directed to adhere the current COVID-19 protocols.
3. The Office is required to inform the concerned Laboratory Staff to arrange the Stationary for the Examination

  
**Dr. M. Tariq Bagday**  
Head of the Department

No: F(DateSheet-M.Tech-2021)

Date: 30/12/2021

 **Copy to:**

1. Web Publication.
2. File
3. Notice Board