



*PG Department of* **ELECTRONICS and  
INSTRUMENTATION TECHNOLOGY**  
**University of Kashmir**

**Notice**

In view of scheduling of examination of Open and Generic (OE/GE) courses of M.Sc. 1<sup>st</sup> semester (Batch 2020), the examination of the course papers titled “**CMOS VLSI and Nano Electronics – I (MOSFET Theory)/ Signals Systems**” and “**Digital Electronics and C Programming Lab**” of M.Sc. 1<sup>st</sup> Semester (Batch 2020) are rescheduled on 21<sup>st</sup> and 24<sup>th</sup> May, 2021 respectively. The timing of the examination shall remain as already notified. All the concerned students are required to attend the examination as per the revised schedule.

***Dr. M. Tariq Bandy***  
Head of the Department

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