



PG Department of **ELECTRONICS and  
INSTRUMENTATION TECHNOLOGY**  
University of Kashmir

**Date Sheet**

*(M. Tech. Embedded Systems and Solutions 1<sup>st</sup> Semester (Regular))*

The Date Sheet for Laboratory Examination of M.Tech. ESS 2<sup>nd</sup> Semester (Regular, batch 2019) of this Department has been scheduled as under:

Time: **10:30 am**

Day and Date	Course No.	Course Title
29 May, 2021 (Saturday)	ESS-207L	ARM Cortex Microcontroller Lab
31 <sup>st</sup> May, 2021 (Monday)	ESS-208L	CPLD and FPGA Lab

*Notes:*

- 1. The Lab examination shall be conducted online as per the issued instructions.*
- 2. All the concerned students are required to attend examination online as per the notified schedule.*
- 3. Dr. Farooq A Kbanday is entrusted with responsibility of nominee of HOD for smooth conduct of examination*

***Dr. M. Tariq Banday***  
Head of the Department

No: F(DateSheet-online-M.Tech-2021)

Date: 27/05/2021

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