



PG Department of ELECTRONICS and INSTRUMENTATION TECHNOLOGY

University of Kashmir

Date Sheet (M. Tech ESS 2nd Semester-Batch 2017)

The Date Sheet for practical Examination of M. Tech ESS 2nd Semester (Regular)-Batch 2017 of this Department has been scheduled as under:

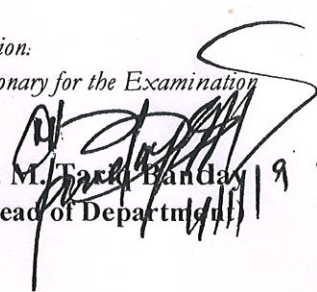
Venue: Department of Electronics & Inst. Technology

Time: 11:00 am

Day and Date	Course No.	Course Title
17 th January, 2019 (Thursday)	ESS207L	ARM Cortex-Microcontroller Lab
19 th January, 2019 (Saturday)	ESS208L	CPLD and FPGA Lab

Notes:

1. All the concerned students are required to attend as per schedule.
2. The concerned Technical Staff are required to be available on the days of Examination.
3. The Office is required to inform the concerned Laboratory Staff to arrange the Stationary for the Examination


Dr. M. Tariq Baidar
(Head of Department)

No: F(DateSheet-M.Tech-2019)

Date: 11/01/2019

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1. Web Publication.
2. File
3. Notice Board