

## PG Department of ELECTRONICS and INSTRUMENTATION TECHNOLOGY

## University of Kashmir

Date Sheet (M. Tech (Embedded Systems and Solutions) 2nd Semester)

The Date Sheet for Examination of M. Tech (Embedded Systems and Solutions) (Batch-2018) 2<sup>nd</sup> Semester (Regular) of this Department has been scheduled as under:

Venue: Department of Electronics & Inst. Technology

Time:	11:00	am

Day and Date	Course No.	Course Title	
19 <sup>th</sup> December,2019 (Thursday)	ESS-204C	CPLD and FPGA Architectures	
. 23 <sup>rd</sup> December,2019 (Monday)	ESS-206E4	Multimedia and Signal Coding	
26 <sup>th</sup> December,2019 (Thursday)	ESS-201R	Research Methodology	
31st December,2019 (Tuesday)	ESS-205C	Embedded System Design with ARM Cortex Microcontrollers	
4 <sup>th</sup> January,2019 (Saturday)	ESS-203C	Advanced Embedded Programming and Languages	

## Notes:

1. All the concerned students are required to attend as per schedule.

2. The concerned Technical Staff/Faculty members are required to be available at 10:00 AM on the days of Examination

Dr. M. Tarid Banday
Head of the Department

No: F(DateSheet-M.Tech-2019)

Date: 26/11/2019

## Copy to:

1. Web Publication.

2. File

3. Notice Board