



P. G. Department of Electronics & Instrumentation Technology
University of Kashmir,
Hazratbal, Srinagar-190006

Ref. No. : F(M.Sc - Backlog)ELE/100/18
Dated : 24/01/2018


Date Sheet (M.Sc. Electronics 3rd Semester)

The Examination for M.Sc. Electronics 3rd semester (backlog) has been scheduled as under:

Venue: Department of Electronics & Inst. Technology

Time: 11:00 am

Day & Date	Course No	Course Title
27 th January, 2018 (Saturday)	ELE15301CR	Physics of Semiconductor Devices
29 th January, 2018 (Monday)	ELE15303CR	Digital Signal Processing
31 st January, 2018 (Wednesday)	ELE15302CR	Control Systems Engineering


(Dr. M. Tariq Baidar)
Head of the Department

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