



PG Department of **ELECTRONICS and
INSTRUMENTATION TECHNOLOGY**

University of Kashmir

Date sheet

The Date Sheet for Examination of *M. Sc. Electronics 3rd Semester (Regular) - Batch 2016* of this Department has been scheduled as under:

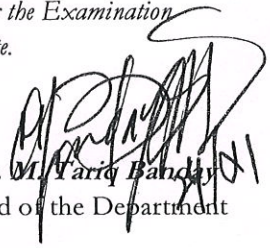
Venue: **Department of Electronics & Inst. Technology**

Time: **11:00 am**

Day and Date	Course No.	Course Title
30 th November, 2017 (Thursday)	ELE-15301C	Physics of Semiconductor Devices
4 th December, 2017 (Monday)	ELE-15305DCE	Microcontrollers Architecture Interfacing and Programming
7 th December, 2017 (Thursday)	ELE-15304C	Computer Networks
12 th December, 2017 (Tuesday)	ELE-15302C	Control System Engineering
18 th December, 2017 (Monday)	ELE-15306DCE/ ELE-15309DCE	Advanced Communication System/CMOS VLSI and Nano Electronics-III (Analog and Mixed IC Design)
21 st December, 2017 (Thursday)	ELE-15303C	Digital Signal Processing

Notes:

1. *All the concerned students are required to attend as per schedule.*
2. *The concerned Technical Staff/Faculty members are required to be available at 10:00 AM on the days of Examination*
3. *The Office is required to inform the concerned Laboratory Staff to arrange the Stationary for the Examination*
4. *Lab Examination for Courses having Laboratory Component shall be held on the same date.*


Dr. M. Tariq Baidar
Head of the Department

No: F(DateSheet-MSC-2017)

Date: 25/11/2017

Copy to:

1. Departmental Website.
2. File.
3. Notice Board.