



PG Department of **ELECTRONICS and
INSTRUMENTATION TECHNOLOGY**
University of Kashmir

Date Sheet

The Date Sheet for Examination of 2nd Semester (Backlog batch 2015) generic elective (GE) courses offered by the Department has been scheduled as under

Venue: **Department of Electronics & Inst. Technology**

Day and Date	Course No.	Course Title	Examination Timing
28 th December, 2017	ELE-15215GE ELE-15218GE	CMOS VLSI and Nano-Electronics – II (Digital IC Design) Data Structures	2.00PM

Notes:

1. *All the concerned students are required to attend as per schedule.*
2. *The concerned Technical Staff/Faculty members are required to be available at 10:00 AM on the days of Examination*
3. *The Office is required to inform the concerned Laboratory Staff to arrange the Stationary for the Examination*
4. *Lab Examination for Courses having Laboratory Component shall be held on the same date.*

Dr. M. Tariq Bandy
Head of the Department

No: F(DateSheet-MSC-2017)

Date: 26/12/2017

Copy to:

1. Departmental Website.
2. File.
3. Notice Board.