



PG Department of **ELECTRONICS and
INSTRUMENTATION TECHNOLOGY**
University of Kashmir

No.: *F (Date sheet - M.Sc) Elec/20/18*

Date: *22-05-2018*

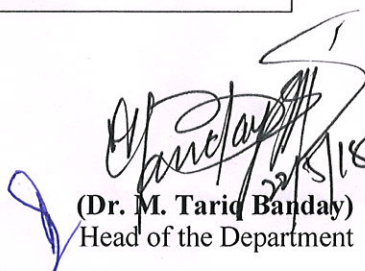
Date Sheet (M.Sc. Electronics level 2 backlog)

The Examination for M.Sc. Electronics level 2 (backlog) has been scheduled as under:

Venue: **Department of Electronics & Inst. Technology**

Time: 11:00 am

Day & Date	Course No	Course Title
28-05- 2018 (Monday)	ELE14205EA	Computer Organization and Architecture
	ELE15209DCE	Simulation and Modeling using MATLAB
	ELE15207DCE	CMOS VLSI and Nano-Electronics-II (Digital IC)
30-05-2018 (Wednesday)	ELE14207EA	Fiber Optic Communication
	ELE15203CR	Power Electronics Circuits and Systems
	ELE15204CR	Microwave Engineering
02-06-2018 (Saturday)	ELE15205DCE	VLSI Technology


(Dr. M. Tariq Bandy)
Head of the Department

Copy to:

1. Departmental Website.
2. File.