



PG Department of **ELECTRONICS and
INSTRUMENTATION TECHNOLOGY**

University of Kashmir

Date Sheet (*M. Sc. Electronics 4th Semester-Batch 2014*)

The Date Sheet for Examination of *M. Sc. Electronics 4th Semester (Backlog)-Batch 2014* of this Department has been scheduled as under:


Venue: **Department of Electronics & Inst. Technology**

Time: **11:00 am**

Day and Date	Course No.	Course Title
18 th November, 2017 (Wednesday)	ELE-14401CR	Digital Communication and Information Theory
22 nd November, 2017 (Wednesday)	ELE-14402CR	VLSI Technology

Notes:

1. *All the concerned students are required to attend as per schedule.*
2. *The concerned Technical Staff/Faculty members are required to be available at 10:00 AM on the days of Examination*
3. *The Office is required to inform the concerned Laboratory Staff to arrange the Stationary for the Examination*


13/11/17
Dr. M. Tariq Bandy
Head of the Department

No: F(DateSheet-MSc-2017)
Date: 13/11/2017

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1. Web Publication.
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3. Notice Board