



PG Department of ELECTRONICS and
INSTRUMENTATION TECHNOLOGY
University of Kashmir

Date Sheet

(M. Sc. Electronics 2nd Semester (Backlog))

The Date Sheet for Examination of M. Sc. (Electronics) 2nd Semester (backlog) of this Department has been scheduled as under:

Venue: Department of Electronics & Inst. Technology

Time: 11:00 am

Day and Date	Course No.	Course Title
13 th March, 2019 (Wednesday)	ELE-15205DCE	VLSI Technology

Notes:

1. All the concerned students are required to attend as per schedule.
2. The concerned Technical Staff/ Faculty members are required to be available at 10:00 AM on the days of Examination
3. The Office is required to inform the concerned Laboratory Staff to arrange the Stationary for the Examination

Dr. M. Farooq Bandyopadhyay
Head of the Department

No: F(DateSheet-MSc-2019)

Date: 06/03/2019

Copy to:

1. Web Publication.
2. File.
3. Notice Board