



PG Department of **ELECTRONICS and
INSTRUMENTATION TECHNOLOGY**
University of Kashmir

Date Sheet (M. Sc. Electronics 4th Semester (Backlog))

The Date Sheet for Examination of M. Sc. (Electronics) 4th Semester Backlog of this Department has been scheduled as under:

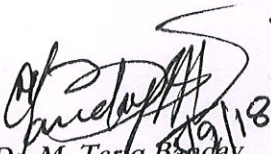
Venue: **Department of Electronics & Inst. Technology**

Time: **11:00 am**

Day and Date	Course No.	Course Title
13 th September, 2018 (Monday)	ELE14402CR ELE402	VLSI Technology

Notes:

All the concerned students are required to attend as per schedule.


Dr. M. Tariq Baidar
Head of the Department

No: F(DateSheet-MSC-2018)

Date: 04/09/2018

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