

Post Graduate Department of Electronics & Instrumentation Technology
University of Kashmir
TIME TABLE 2nd Semester (Five Year Integrated Masters Programme (FYIMP) in Electronics Batch 2025)
Effective from 23rd February 2026 (Room 1)

| Days/Time | 10:00 am – 11:00 am | 11:00 am – 12:00 pm | 12:00 pm – 12:30 pm | 12:30 pm – 2:30 pm | 2:30 pm – 3:30 pm | 3:30 pm – 4:30 pm |
|-----------|--|---|----------------------|--|--------------------------------|----------------------------------|
| Monday | Digital Integrated Electronics (Ms. Farhat Roohie) | Programming for AI (Prof. M. Tariq Banday/Dr. Javeed Iqbal Reshi) | BREAK | Digital Integrated Electronics LAB Supervisor: Dr. Shabir Ahmad Parah Teachers: Ms. Farhat Roohie, Dr. Gul Faroz A. Malik, Dr. Nasir Nabi Hurrah | Multidisciplinary Course (MDC) | Ability Enhancement Course (AEC) |
| Tuesday | Digital Integrated Electronics (Ms. Farhat Roohie) | Programming for AI (Prof. M. Tariq Banday/Dr. Javeed Iqbal Reshi) | | Digital Integrated Electronics LAB Supervisor: Dr. Shabir Ahmad Parah Teachers: Ms. Farhat Roohie, Dr. Gul Faroz A. Malik, Dr. Nasir Nabi Hurrah | Multidisciplinary Course (MDC) | Ability Enhancement Course (AEC) |
| Wednesday | Digital Integrated Electronics (Ms. Farhat Roohie) | Physics (Dr. Zamir Ahmad Wani) | | Consumer Electronics (Home Appliances) LAB Supervisors: Dr. Javaid Ahmad Sheikh, Teachers: Dr. Nasir Nabi Hurrah | Multidisciplinary Course (MDC) | Ability Enhancement Course (AEC) |
| Thursday | Consumer Electronics (Office Appliances) (Dr. Javaid Ahmad Sheikh/Dr. Nasir Nabi Hurrah) | Physics (Dr. Zamir Ahmad Wani) | | Programming for AI LAB Supervisors: Prof. M. Tariq Banday, Teachers: Dr. Javeed Iqbal Reshi | Value Added Courses (VAC1) | Value Added Courses (VAC2) |
| Friday | Consumer Electronics (Office Appliances) (Dr. Javaid Ahmad Sheikh/Dr. Nasir Nabi Hurrah) | Programming for AI (Prof. M. Tariq Banday/Dr. Javeed Iqbal Reshi) | Library Consultation | Programming for AI LAB Supervisors: Prof. M. Tariq Banday, Teachers: Dr. Javeed Iqbal Reshi, Dr. Gul Faroz A. Malik | Value Added Courses (VAC1) | Value Added Courses (VAC2) |

1. All concerned faculty / Notice Board
2. File


Dr. Farooq A. Khanday
 (Head of the Department)

Post Graduate Department of Electronics & Instrumentation Technology
University of Kashmir

**TIME TABLE 3rd Semester (Five Year Integrated Masters Programme (FYIMP) in Electronics Batch 2024)
Effective from 23rd February 2026 (Room 3)**

| Days/Time | 10:00 am – 11:00 am | 11:00 am – 12:00 pm | 12:00 pm – 12:30 pm | 12:30 pm – 2:30 pm | 2:30 pm – 3:30 pm | 3:30 pm – 4:30 pm |
|-----------|--|--|---------------------------|--|--|----------------------------------|
| Monday | Operational Amplifiers and Applications (Dr. Farooq Ahmad Khanday) | Introduction to AI and Machine Learning (Dr. Javaid Ahmad Sheikh) | BREAK | Operational Amplifiers and Applications LAB Supervisor: Dr. Farooq Ahmad Khanday Teachers: Dr. Asif Bashir Dar | Multidisciplinary Course (MDC) | Ability Enhancement Course (AEC) |
| Tuesday | Operational Amplifiers and Applications (Dr. Farooq Ahmad Khanday) | Introduction to AI and Machine Learning (Dr. Javaid Ahmad Sheikh) | | Operational Amplifiers and Applications LAB Supervisor Dr. Farooq Ahmad Khanday Teachers: Dr. Asif Bashir Dar | Multidisciplinary Course (MDC) | Ability Enhancement Course (AEC) |
| Wednesday | Operational Amplifiers and Applications (Dr. Farooq Ahmad Khanday) | Introduction to AI and Machine Learning (Dr. Javaid Ahmad Sheikh) | | Consumer Electronics (Home Appliances) LAB Supervisor: Dr. Shabir Ahmad Parah Teachers: Dr. Zamir Ahmad Wani | Multidisciplinary Course (MDC) | Ability Enhancement Course (AEC) |
| Thursday | Consumer Electronics (Communication Appliances) (Dr. Shabir Ahmad Parah/Dr. Zamir Ahmad Wani) | Introduction to AI and Machine Learning (Dr. Javaid Ahmad Sheikh) | | Introduction to AI and Machine Learning LAB Supervisor: Dr. Javaid Ahmad Sheikh, Teachers: Dr. Nasir Nabi Hurrah | Value Added Courses (VAC1) | Value Added Courses (VAC2) |
| Friday | Operational Amplifiers and Applications (Dr. Farooq Ahmad Khanday) | Consumer Electronics (Communication Appliances) (Dr. Shabir Ahmad Parah/Dr. Zamir Ahmad Wani) | | Library Consultation | Introduction to AI and Machine Learning LAB Supervisor: Dr. Javaid Ahmad Sheikh, Teachers: Dr. Nasir Nabi Hurrah, Dr. Asif Bashir Dar | Value Added Courses (VAC1) |

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P. G. Department of Electronics & Instrumentation Technology

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Time Table (Theory and laboratory Sessions)

M. Tech ESS Semester-2nd (Batch 2025)

Effective from 23rd February 2026 (Room 2)

| Days/Time | 10:00 am – 11:00 am | 11:00 am – 12:00 pm | 12:00 pm – 1:00pm | 1:00 pm – 1:30 pm | 1:30 pm – 2:00 pm | 2:00 pm – 3:15 pm | 3:15 pm – 4:30 pm |
|-----------|---|---|--|---|-------------------------|--|-------------------|
| Monday | Embedded System Design with ARM Cortex Microcontrollers (Prof. M. Tariq Bandy / Dr. Javeed Iqbal Reshi) | Research Methodology (Dr. Zamir A. Wani) | Advanced Embedded Programming Language (Dr. Asif Bashir Dar) | Library Consultation and Group Discussion | BREAK | ARM Cortex Microcontroller LAB Supervisor: Prof. M. Tariq Bandy Teachers: Dr. Javeed Iqbal Reshi | |
| Tuesday | Embedded System Design with ARM Cortex Microcontrollers (Prof. M. Tariq Bandy / Dr. Javeed Iqbal Reshi) | Research Methodology (Dr. Zamir A. Wani) | CPLD and FPGA Architectures (Dr. Farooq Ahmad Khanday/ Dr. Gul Faroz A. Malik) | | | ARM Cortex Microcontroller LAB Supervisor: Prof. M. Tariq Bandy Teachers: Dr. Javeed Iqbal Reshi | |
| Wednesday | Embedded System Design with ARM Cortex Microcontrollers (Prof. M. Tariq Bandy / Dr. Javeed Iqbal Reshi) | Multimedia and Signal Coding (Dr. Shabir Ahmad Parah) | CPLD and FPGA Architectures (Dr. Farooq Ahmad Khanday/ Dr. Gul Faroz A. Malik) | | | CPLD and FPGA Architectures LAB Supervisor: Dr. Farooq Ahmad Khanday Teachers: Dr. Gul Faroz A. Malik, Dr. Asif Bashir Dar | |
| Thursday | CPLD and FPGA Architectures (Dr. Farooq Ahmad Khanday/ Dr. Gul Faroz A. Malik) | Multimedia and Signal Coding (Dr. Shabir Ahmad Parah) | Advanced Embedded Programming Language (Dr. Asif Bashir Dar) | | | CPLD and FPGA Architectures LAB Supervisors: Dr. Farooq Ahmad Khanday Teachers: Dr. Gul Faroz A. Malik, Dr. Asif Bashir Dar | |
| Friday | Embedded System Design with ARM Cortex Microcontrollers (Prof. M. Tariq Bandy / Dr. Javeed Iqbal Reshi) | Multimedia and Signal Coding (Dr. Shabir Ahmad Parah) | Advanced Embedded Programming Language (Dr. Asif Bashir Dar) | | | Multimedia and Signal Coding LAB Supervisors: Dr. Shabir Ahmad Parah, Teachers: Dr. Nasir Nabi Hurrah | |

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Time Table (Project Work)
M. Tech ESS Semester-IV (Batch 2023) Effective from 23rd February 2026

| Course Code | Students Assigned | Supervisor/Co-Supervisor | Hours Per Week |
|-------------|------------------------|---------------------------|----------------|
| ESS-401T | 02, 19, 21, 23 | Prof. M. Tariq Banday | 5 |
| | | Ms. Farhar Roohie | 5 |
| | | Dr. Javeed Iqbal reshi | 5 |
| | 01, 03, 05, 17, 18, 21 | Dr. Farooq A Khanday | 5 |
| | | Dr. Gul Faroz Ahmad Malik | 5 |
| | | Dr. Asif Bashir Dar | 5 |
| | 04, 07,13, 16, 24 | Dr. Shabir A Parah | 5 |
| | | Dr. Nasir Nabi Hurrah | 5 |
| | | Dr. Asif Bashir Dar | 5 |
| | 06, 08, 09, 12, 20 | Dr. Javaid A Sheikh | 5 |
| | | Dr. Zamir Ahmad Wani | 5 |
| | | Dr. Nasir Nabi Hurrah | 5 |

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