



PG Department of **ELECTRONICS and INSTRUMENTATION TECHNOLOGY**

University of Kashmir

Date Sheet

M.Tech (Embedded Systems and Solutions) 2nd Semester.

The Date Sheet for Examination of M.Tech (Embedded Systems and Solutions) 2nd Semester batch 2023(Regular / backlog) of this Department has been scheduled as under:


Venue: Department of Electronics & Inst. Technology

Time: 10:30AM

Day and Date	Course No.	Course Title
12 th December,2024 (Thursday)	(ESS-204C)	CPLD and FPGA Architectures
16 th December,2024 (Monday)	(ESS-203C)	Advanced Embedded Programming Languages
19 th December,2024 (Thursday)	(ESS-201R)	Research Methodology
23 rd December,2024 (Monday)	ESS-205C	Embedded System Design with ARM Cortex Microcontroller
26 th December,2024 (Thursday)	(ESS-206E4)	Multimedia And Signal Coding
28 th December,2024 (Saturday)	(ESS-207L)	ARM Cortex Microcontroller Lab
30 th December,2024 (Monday)	(ESS-208L)	CPLD and FPGA Lab

Notes:

1. All the concerned students are required to attend as per schedule.
2. The concerned Faculty members are required to be available at 10:00 AM on the days of Examination
3. Lab Examination for Courses having Laboratory Component shall be held on the same date.
4. The Office is required to inform the concerned Laboratory Staff to arrange the Stationary for the Examination
5. Web Publication.
6. File.
7. Notice Board


20/11/2024
M. Farid Baidar
20/11/2024
(Prof. M. Farid Baidar)
Head of the Department