PG Department of ELECTRONICS and INSTRUMENTATION TECHNOLOGY
University of Kashmir

Date sheet

The Date Sheet of Five Year Integrated Masters Programme (FYIMP) in Electronics batch 2024 1st Semester of this Department has been scheduled as under:

Venue: **Department of Electronics & Inst. Technology**   Time: **10:30AM**

Day and Date	Course No.	Course Title
04-07-2025	(IELEMJNA0124)	Network Analysis & Analog Electronics.
07-07-2025	(CEL122S)	Consumer Electronics.
10-07-2025	(IELEIMCI0124)	Mathematics for AI.

(Prof. M. Tariq Banday)
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Notes:

1. All the concerned students are required to attend as per schedule.
2. The concerned Faculty members are required to be available at 10:00 AM on the days of Examination
3. Lab Examination for Courses having Laboratory Component shall be held on the same date.
4. The Office is required to inform the concerned Laboratory Staff to arrange the Stationary for the Examination
5. Web Publication.
6. File.
7. Notice Board