



**P. G. Department of Electronics & Instrumentation Technology**  
**University of Kashmir,**  
Hazratbal, Srinagar-190006

Ref. No. : F(Ds-M-Tech)ess /

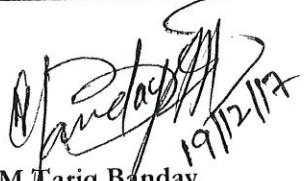
Dated : 19-12-2017

**DATE SHEET**

The laboratory examination of the M.Tech (Embedded Systems and Solutions) 2<sup>nd</sup> Semester (batch 2016) shall be held as per the following schedule:

**Time: 11.00 A.M**

Serial No.	Subject Title and Code	Date of Examination
01.	CPLD and FPGA Lab (ESS-208L)	23-12-2017 (Saturday)
02.	ARM Cortex Microcontroller Lab (ESS-207L)	27-12-2017 (Wednesday)

  
**Dr M. Tariq Bandy**  
(Head of the Department)

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