



PG Department of **ELECTRONICS and INSTRUMENTATION TECHNOLOGY**  
University of Kashmir

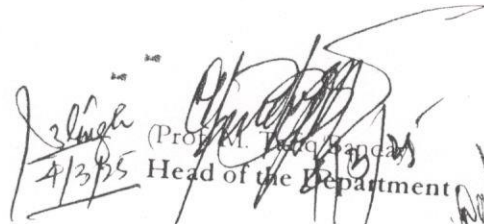
Date sheet


The Date Sheet of M.Sc 1<sup>st</sup> Semester Fresh (Batch 2024)/Backlog of this Department has been scheduled as under:

Venue: Department of Electronics & Inst. Technology

Time: 10:30am

Day and Date	Course No.	Course Title
10 <sup>th</sup> March, 2025 (Monday)	(ELE19101CR)	Circuit Analysis and Synthesis
13 <sup>th</sup> March, 2025 (Thursday)	(ELE19102CR)	Antennas and Wave Propagation
17 <sup>th</sup> March 2025 (Monday)	(ELE19103CR)	Linear Integrated Circuit & Applications (LICA)
20 <sup>th</sup> March, 2025 (Thursday)	ELE19104CR)	Digital Electronics And C-Programming Lab
24 <sup>th</sup> March, 2025 (Thursday)	(ELE19105DCE)	Engineering Mathematics
27 <sup>th</sup> March, 2025 (Thursday)	(ELE19106DCE)	CMOS VLSI & Nano Electronics -I

  
(Prof. M. Tariq Bhandari)  
Head of the Department

  
04-03-2025

Notes:

1. All the concerned students are required to attend as per schedule.
2. The concerned Faculty members are required to be available at 10:00 AM on the days of Examination
3. Lab Examination for Courses having Laboratory Component shall be held on the same date.
4. The Office is required to inform the concerned Laboratory Staff to arrange the Stationary for the Examination