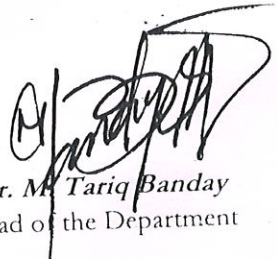




PG Department of **ELECTRONICS and  
INSTRUMENTATION TECHNOLOGY**  
University of Kashmir

Notice

It is notified for the information of all the students of M.Tech. ESS 3<sup>rd</sup> Semester (batch 2020), that the examination for the papers titled *Digital Signal Processor and Architecture* (ESS 301C) which was postponed shall now be held on 16<sup>th</sup> February, 2023. The timing and venue of the examination shall remain the same. All the concerned students are required to attend as per schedule.

  
Dr. M. Tariq Banday  
Head of the Department

No. F:(DateSheet-MSC-2023)  
Date: 13/02/2023

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