

PG Department of ELECTRONICS and INSTRUMENTATION TECHNOLOGY

University of Kashmir

No: F(Viva-Voce/Phel) Elech Date: 30-07-2021

NOTICE

The viva-voce examination of one of the Ph.D scholar namely (Mr. Hilal Ahmad Bhat), of this Department who has been pursuing his Ph.D. Programme on the topic entitled "Design and Implementation of Efficient Spin Quantum Computing Arithmetic Circuits", has been scheduled on 05th August 2024 (Monday) at 12:00 Noon. All DRC members, faculty members, and research scholars are informed to attend the said viva-voce examination.

Copy to:

- 1. All Faculty members.
- 2. All research Laboratories.
- 3. Notice board.
- 4. File