

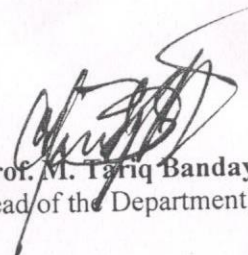
# Post Graduate Department of Electronics & Instrumentation Technology

University of Kashmir

**TIMETABLE(Five Year Integrated Masters Programme(FYIMP) in Electronics Batch 2024)**

Effective from 26<sup>th</sup> May 2025

| Days/Time        | 9:30 am –10:30 am                                                   | 10:30 am–11:30 am                                                 | 11:30pm –12:30pm                                          | 12:30pm–1:30pm                                       | 1:30pm–2:00pm | 2:00pm–3:00pm                                                                                                                                              | 3:00pm– 4:00pm | 4:00pm–5:00pm |
|------------------|---------------------------------------------------------------------|-------------------------------------------------------------------|-----------------------------------------------------------|------------------------------------------------------|---------------|------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|---------------|
| <b>Monday</b>    | Network Analysis & Analog Electronics<br>(Dr. Farooq Ahmad Khanday) | Network Analysis & Analog Electronics<br>(Dr. Shabir Ahmad Parah) | Essential Mathematics for AI<br>(Dr. Baseerat Gul)        | Library Consultation, Tutorials and Group Discussion | BREAK         | Network Analysis & Analog Electronics LAB<br>Supervisor: Dr. Shabir Ahmad Parah<br>Teachers: Dr. Baseerat Gull, Dr. Nasir Nabi Hurah                       |                |               |
| <b>Tuesday</b>   | Network Analysis & Analog Electronics<br>(Dr. Farooq Ahmad Khanday) | Network Analysis & Analog Electronics<br>(Dr. Shabir Ahmad Parah) | Essential Mathematics for AI<br>(Dr. Baseerat Gul)        |                                                      |               | Network Analysis & Analog Electronics LAB<br>Supervisor: Dr. Farooq Ahmad Khanday<br>Teachers: Dr. Javeed Iqbal Reshi, Dr. Hilal Ahmad Bhat                |                |               |
| <b>Wednesday</b> | Network Analysis & Analog Electronics<br>(Dr. Farooq Ahmad Khanday) | Consumer Electronics (Home Appliances)<br>(Dr. Nazir A. Lone)     | Essential Mathematics for AI<br>(Dr. Javaid Ahmad Sheikh) |                                                      |               | Consumer Electronics (Home Appliances) LAB<br>Supervisors: Prof. M. Tariq Banday, Dr. Javaid A Sheikh<br>Teachers: Dr. Nazir A. Lone, Dr. Hilal Ahmad Bhat |                |               |
| <b>Thursday</b>  | Network Analysis & Analog Electronics<br>(Dr. Farooq Ahmad Khanday) | Network Analysis & Analog Electronics<br>(Dr. Shabir Ahmad Parah) | Essential Mathematics for AI<br>(Dr. Javaid Ahmad Sheikh) |                                                      |               | Consumer Electronics (Home Appliances) LAB<br>Supervisors: Prof. M. Tariq Banday, Dr. Javaid A Sheikh<br>Teachers: Dr. Nazir A. Lone, Dr. Mehboob-ul Amin  |                |               |
| <b>Friday</b>    | Network Analysis & Analog Electronics<br>(Dr. Farooq Ahmad Khanday) | Consumer Electronics (Home Appliances)<br>(Dr. Nazir A. Lone)     | Essential Mathematics for AI<br>(Dr. Baseerat Gul)        |                                                      |               | Network Analysis & Analog Electronics LAB<br>Supervisor: Dr. Farooq Ahmad Khanday<br>Teachers: Dr. Javeed Iqbal Reshi, Dr. Hilal Ahmad Bhat                |                |               |

  
**Prof. M. Tariq Banday**  
 (Head of the Department)

1. All concerned faculty/Notice Board
2. File