



PG Department of **ELECTRONICS and
INSTRUMENTATION TECHNOLOGY**
University of Kashmir

Date sheet

The Date Sheet of *Five-Year Integrated Masters Programme (FYIMP) in Electronics*, Batch 2025 2nd Semester of this Department has been scheduled as under:

Venue: Department of Electronics & Inst. Technology

Time: 10:30AM

Day and Date	Course No.	Course Title
20 th July,2026 (Monday)	(IELEMJDE0224)	Digital Integrated Electronics
23 rd July,2026 (Thursday)	(IELESECE0224)	Consumer Electronics (office Appliance)
27 th July,2026 (Monday)	(IELEMNPA0224)	Programming for AI


(Dr. Farooq A. Khanday)
Head of the Department

Notes:

1. All the concerned students are required to attend as per schedule.
2. The concerned Faculty members are required to be available at 10:00 AM on the days of Examination
3. Lab Examination for Courses having Laboratory Component shall be held on the same date.
4. The Office is required to inform the concerned Laboratory Staff to arrange the Stationary for the Examination
5. Web Publication.
6. File.
7. Notice Board