



PG Department of ELECTRONICS and
INSTRUMENTATION TECHNOLOGY
University of Kashmir

Date Sheet (M. Sc. Electronics 1st Semester-Batch 2019)

The Date Sheet for Examination of M. Sc. Electronics 1st Semester (Regular/Backlog)-Batch 2019 of this Department has been scheduled as under:

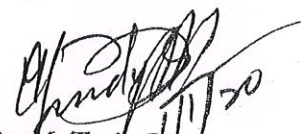
Venue: Department of Electronics & Inst. Technology

Time: 11:00 a.m

Day and Date	Course Title	Course No.
18 th January, 2020 (Saturday)	Circuit Analysis and Synthesis	ELE19101C
21 st January, 2020 (Tuesday)	Antennas and Wave Propagation	ELE19102C
25 th January, 2020 (Saturday)	Linear Integrated Circuits and Applications (LICA)	ELE19103C
28 th January, 2020 (Tuesday)	CMOS VLSI and Nano-Electronics –I(MOSFET Theory) Signals and Systems	ELE19106 DCE ELE19107 DCE,
1 st February, 2020 (Saturday)	Engineering Mathematics	ELE19105 DCE
4 th February, 2020 (Tuesday)	Digital Electronics and C programming Lab	ELE19104C

Notes:

1. All the concerned students are required to attend as per schedule.
2. The concerned Technical Staff/Faculty members are required to be available at 10:00 AM on the days of Examination
3. The Office is required to inform the concerned Laboratory Staff to arrange the Stationary for the Examination


Dr. M. Tariq Bhanday
Head of the Department

No: F(DateSheet-MSc-2020)

Date: 04/01/2020

Copy to:

1. PRO for publishing the said date sheet in the news paper
2. Web Publication.
3. File.
4. Notice Board