Date Sheet

M.Tech (Embedded Systems and Solutions) 1ˢᵗ Semester (backlog) up to 2024

The Date Sheet for Examination of *M.Tech (Embedded Systems and Solutions) 1ˢᵗ Semester* backlog up to 2024 of this Department has been scheduled as under:

Venue: **Department of Electronics & Inst. Technology**   Time: **10:30 AM**

Day and Date	Course No.	Course Title
4ᵗʰ July,2025 (Friday)	(ESS-105C)	Advanced Digital System Design.
7th July ,2025 (Monday)	(ESS-104C)	Microcontroller for Embedded System Design.
10ᵗʰ July,2025 (Thursday)	(ESS-103C)	Fundamentals of Embedded System.

Notes:

1. All the concerned students are required to attend as per schedule.
2. The concerned Faculty members are required to be available at 10:00 AM on the days of Examination
3. The Office is required to inform the concerned Laboratory Staff to arrange the Stationary for the Examination
4. Web Publication.
5. File.
6. Notice Board

(Prof. M. Tariq Banday)
Head of the Department