PG Department of ELECTRONICS and INSTRUMENTATION TECHNOLOGY

University of Kashmir

Date Sheet

M.Tech (Embedded Systems and Solutions) 2nd Semester.

The Date Sheet for Examination of *M.Tech (Embedded Systems and Solutions), 2nd Semester batch 2021*(Regular / backlog) of this Department has been scheduled as under:

Venue: **Department of Electronics & Inst. Technology**   Time: **10:00 am**

Day and Date	Course No.	Course Title
20th May,2024 (Monday)	(ESS-205C)	Embedded System Design with ARM Cortex Microcontroller
24th May,2024 (Friday)	(ESS-204C)	CPLD and FPGA Architectures
27th May,2024 (Monday)	(ESS-201R)	Research Methodology
31th May,2024 (Friday)	(ESS-206E4)	Multimedia and Signal Coding
04th June,2024 (Tuesday)	(ESS-203C)	Advanced Embedded Programming Languages
06th June,2024 (Thursday)	(ESS-208L)	ARM Cortex Microcontroller Lab
07th June,2024 (Friday)	(ESS-207L)	CPLD and FPGA Lab

Notes:

1. All the concerned students are required to attend as per schedule.
2. The concerned Faculty members are required to be available at 10:00 AM on the days of Examination
3. Lab Examination for Courses having Laboratory Component shall be held on the same date.
4. The Office is required to inform the concerned Laboratory Staff to arrange the Stationary for the Examination
5. Web Publication.
6. File.
7. Notice Board

(Prof. M. Tariq Banday)
Head of the Department