*PG Department of* **ELECTRONICS** *and*
# INSTRUMENTATION TECHNOLOGY
## University of Kashmir

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| **Date Sheet** | No:F(DateSheet-M.Tech-2022) |
| | Date: 18/10/2024 |

*(M.Tech (Embedded Systems and Solutions) 3rd Semester Batch 2022*

The Date Sheet for Examination of *M.Tech (Embedded Systems and Solutions) 3rd Semester* (Regular/Backlog) Batch 2022 of this Department has been scheduled as under:

**Venue: Department of Electronics & Inst. Technology**          Time: **10:00 am**

| Day and Date | Course No. | Course Title |
|---|---|---|
| 21st November, 2024 (Thursday) | (ESS-301C) | Mixed Signal Embedded Systems |
| 25th November, 2024 (Monday) | (ESS-302C) | Embedded System Security and Forensics |
| 29th November, 2024 (Friday) | (ESS-303E1) | Digital Signal Processor and Architecture |
| 3rd December, 2024 (Tuesday) | (ESS-304E1) | Internet of Things |
| 06th December, 2024 (Friday) | (ESS-305E2) | Digital Communication Techniques |
| 09th December, 2024 (Monday) | (ESS-306L) | Mixed signal and DSP Lab |

*Notes:*

1. *All the concerned students are required to attend as per schedule.*
2. *The concerned Faculty members are required to be available at 10:00 AM on the days of Examination*
3. *Lab Examination for Courses having Laboratory Component shall be held on the same date.*
4. *The Office is required to inform the concerned Laboratory Staff to arrange the Stationary for the Examination*
5. *Web Publication.*
6. *File.*
7. *Notice Board*

**(Prof. M. Tariq Banday)**
Head of the Department