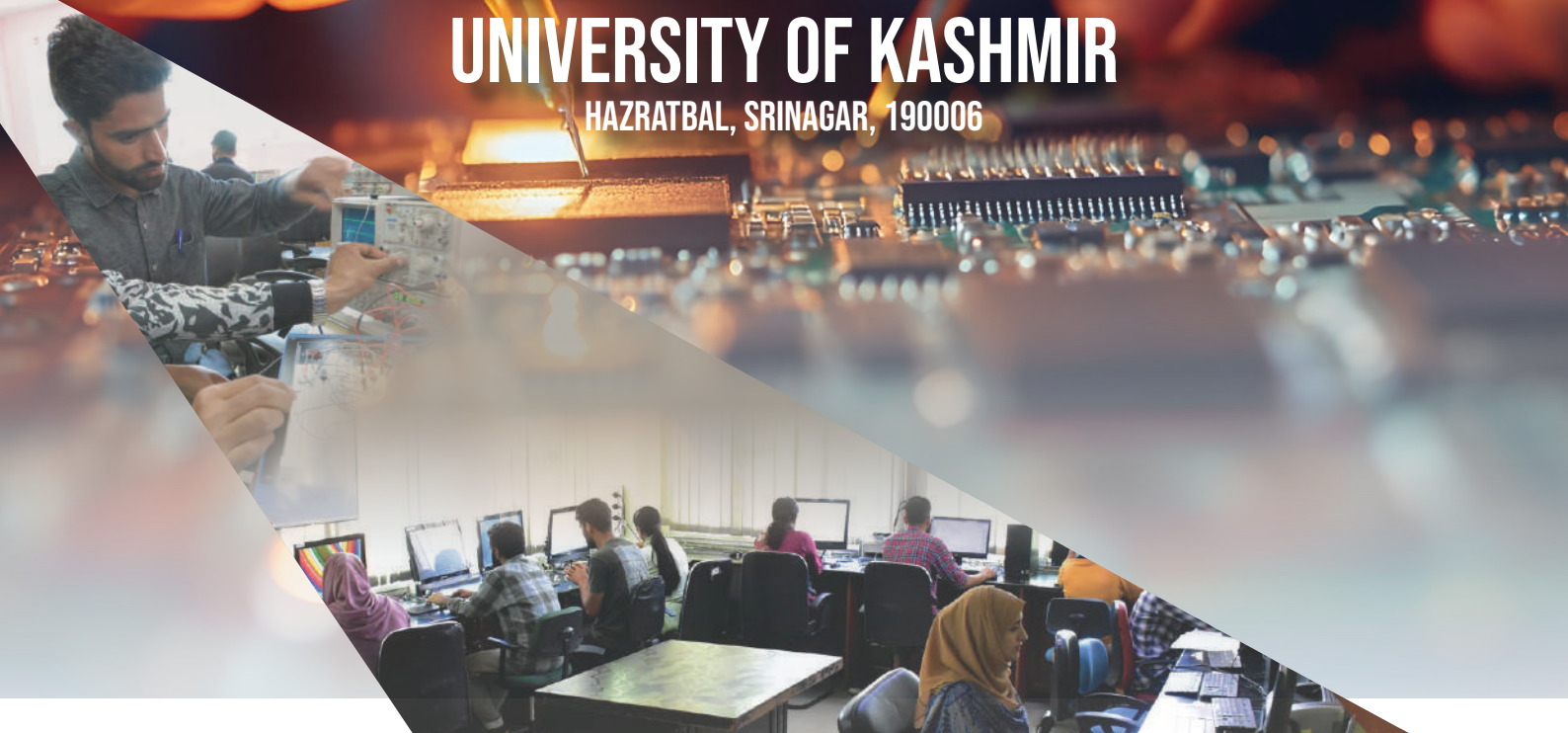




**POST GRADUATE DEPARTMENT OF ELECTRONICS
AND INSTRUMENTATION TECHNOLOGY
UNIVERSITY OF KASHMIR
HAZRATBAL, SRINAGAR, 190006**



**DEVELOPING
LEADERS IN THE
EVER-EVOLVING
FIELD OF
ELECTRONICS**

**FIVE-YEAR INTEGRATED
MASTER'S PROGRAMME (FYIMP)
IN ELECTRONICS**

**A MULTI-ENTRY, MULTI-EXIT PROGRAMME WITH COMPREHENSIVE
ELECTRONICS AND ARTIFICIAL INTELLIGENCE CURRICULUM**

ASPIRE. INSPIRE. AND LEAD!

Gain an AI edge with our Master's in Electronics. Specialize, learn, and excel

Five-Year Integrated Master's Programme in Electronics

A multi-entry, multi-exit programme with comprehensive Electronics and Artificial Intelligence curriculum

This program offers a rich tapestry of coursework, fostering a well-rounded understanding of the field. Students gain a strong foundation in core electronics through dedicated courses. The programme allows specializations in **emerging areas of Electronics such as VLSI, Signal Processing, Communications, Embedded Systems, Computing, Image Processing, Nanoelectronics, Wireless Communications, Wearable Electronics, Internet of Things, Multimedia Signal Coding, Neuromorphic Computing, etc.** The programme also includes contemporary courses in ever-evolving fields of computation and Artificial Intelligence. Skill-oriented courses equip them with practical abilities valued by the industry. Multidisciplinary courses encourage a holistic approach by integrating knowledge from various disciplines. Additionally, ability enhancement courses hone communication, critical thinking, and problem-solving skills – essential assets for success. This comprehensive and industry-oriented education empowers graduates to become future leaders in the dynamic field of electronics. Graduates can pursue rewarding careers in various sectors, including industry, academia, and business entrepreneurship. Graduates in Electronic Sciences can go on to serve in industries or may opt to establish their Start-Ups. They can also be recruited directly in MNCs after their completion.

Overview

- Flexible Learning Paths:** The programme offers a multi-entry, multi-exit structure, allowing students to join and leave at various stages. Earn qualifications ranging from certificates to integrated master's degrees based on their individual goals and pace. Students can re-enter the program within three years of exiting, providing flexibility and opportunities for continued learning.
- Strong Foundation:** The programme includes foundational courses to ensure students have a solid understanding of the basics, setting them up for success in their chosen degree programme.
- Hands-On Learning:** The learning methodologies prioritize practical experience through internships, projects, and problem-based learning, ensuring students are well-prepared for the real world.
- Industry-Relevant Skills:** The programme curriculum stays current with the latest trends and equips students with the in-demand skills sought by the electronics industry.
- Research-Oriented:** The programme fosters a research-oriented mindset, providing students with the skills and knowledge to pursue advanced studies or research careers.
- Cutting-Edge Curriculum:** The programme curriculum is continually updated to reflect the latest advancements in electronics, ensuring graduates have a deep understanding of the field.
- The AI Edge:** The programme includes contemporary courses in ever-evolving fields of computation and Artificial Intelligence.

Programme entry and exit points

Entry Year	Exit Year	Certificate/Diploma/Degree
1 st	1 st	Certificate in Electronics
	2 nd	Diploma in Electronics
	3 rd	Bachelor's Degree in Electronics
	4 th	Bachelor's Degree (Honors) with or without Research in Electronics
	5 th	Five-Year Integrated Master's Degree in Electronics
4 th	4 th	PG Diploma in Electronics
	5 th	Two Years Master's Degree in Electronics
5 th	5 th	One Year Master's Degree in Electronics

Eligibility

1st Year (Semester I): Candidates who have Passed the 10+2 examination (Science Subjects) with a minimum of 45% marks in case of general category and 40% marks in case of reserved category candidates from the Jammu and Kashmir Board of School Education or from any other recognized board/ institution whose examinations have been recognized as equivalent to that by the Jammu and Kashmir Board of School Education.

4th Year (Semester VII): Three-year Bachelors' Degree in Electronics from any recognized University under NEP 2020 with a minimum of 45% marks in case of general category and 40% marks in case of reserved category candidates.

5th Year (Semester IX): Four-Year Honors' Degree in Electronics under NEP 2020 or B.E./B.Tech. in Electronics or equivalent with a minimum of 45% marks in case of general category and 40% marks in case of reserved category candidates.



Selection

Admission shall be made through Entrance Test and admission shall be granted on the basis of the merit obtained in the entrance test, conducted by the University.



Intake Capacity

20, 10 and 5 respectively at first, fourth and fifth years excluding self-finance, foreign nationals, supernumerary and vacancies on part of early exits.

Course Fee

General Category: Rs. 11,375/= per year.

Self-Finance Category: A self-finance fee of Rs. 11,375/= per year in addition to fee mentioned for general category.

Foreign Nationals Category: Rs.30,000/= per semester.

An Industrial Training Fee of Rs. 10,000/= shall be charged in the 5th year (9th semester). This Fee component shall be payable by students admitted under all categories.

Examination Fees, Hostel Fees, etc., shall be charged extra as per the University policy in Vogue.





Completed Projects (Individual)

Ongoing Projects (Individual)

Thrust Areas of Research

Sl. No.	Year	Title of the Project	Funding Agency	Principal Investigator	Cost	Status
1.	2013	Design and Development of New Local Security Protocol and Encryption	UPG Govt.	Dr. M. Tariq Bhatnagar	11,27,000/-	Completed
2.	2017	Development of New Filtering Algorithm for Signal Processing	Dr. Farooq A. Bhatnagar	Dr. Farooq A. Bhatnagar	9,30,000/-	Completed
3.	2017	Development of New Filtering Algorithm for Signal Processing	Dr. Farooq A. Bhatnagar	Dr. Farooq A. Bhatnagar	3,00,000/-	Completed

Sl. No.	Year	Title of the Project	Funding Agency	Principal Investigator	Cost in Rupees	Status
1.	2017	Design of Low Pass Filter, Low Complexity, High Pass Filter and Chebyshev Filter for Security Control	Department of Electronics and Instrumentation Technology	Dr. M. Tariq Bhatnagar	47,80,000/-	Ongoing
2.	2017	Development of New Filtering Algorithm for Signal Processing	Department of Electronics and Instrumentation Technology	Dr. Farooq A. Bhatnagar	44,00,000/-	Ongoing
3.	2017	Algorithm and Hardware Design for Fourier Transform	Department of Electronics and Instrumentation Technology	Dr. M. Tariq Bhatnagar	28,55,900/-	Ongoing
4.	2017	Design of New Filtering Algorithm for Signal Processing	Department of Electronics and Instrumentation Technology	Dr. Farooq A. Bhatnagar	43,84,000/-	Ongoing

- Internet of Things, Network Security and Digital Forensics
- High Performance Integrated Circuits and Nano-Electronics
- Biomedical, Fractional System Design
- Low Complexity Multi-Channel System Design
- Wireless Communication and Filter Design
- Micro-controllers and Filter Design

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