



UNIVERSITY OF KASHMIR

HAZRATBAL, SRINAGAR

NAAC ACCREDITED A+

Notification

Sub: Introduction of Five-Year Integrated Master's Programme in Electronics (IMSc-Electronics).

It is notified for information of all concerned that the Vice-Chancellor, in anticipation of the approval of the competent body, has been pleased to authorize introduction of full-time regular course "5 Year Integrated Master's Programme in Electronics (IMSc Electronics)" at the Department of Electronics and Instrumentation Technology, Main Campus. The Eligibility, Mode of selection, Intake, Fee Structure and Credit Frame Work shall be:

- (i) Eligibility 12th Pass with science subjects having secured atleast 50% marks in case of General Category and 45% marks in case of Reserved Category.
- (ii) Mode of Selection Entrance Test conducted by the Directorate of Admissions and Competitive Examinations, University of Kashmir.
- (iii) Intake 26 Seats including Six (06) Self-Finance Seats.
- (iv) Fee Structure
- a. Rs. 11,375/- Per annum for General Category.
BREAK UP:
Part A: Rs. 5800/-
Part B: Rs. 4500/-
Part C: Rs. 1075/-
- b. Rs. 22,750/ per annum including normal Admission Fee for self finance seats.
BREAK UP:
Part A: Rs. 58,00 /-
Part B: Rs. 4500/-
Part C: Rs. 1075/-
Self-finance: Rs.11,375/-
- c. Credit Frame work As per NEP-2020 with a minimum of 20 credits per semester
- d. Industrial Tour/ Internship As decided by the departmental committee with 100% funding by the candidates.
- e. Legal Framework Shall be governed by the Statutes governing University Five year Integrated PG Programmes.

The above shall be effective from the current academic session.

No: (Intro-IMSc Electronics)Acad/KU/2024

Dated: 12-08-2024

Copy for information to:-

1. Dean, Academic Affairs, University of Kashmir, Srinagar. This bears reference to approval of V.C conveyed vide No. 597/ACDF, dated: 15-07-24.
2. Dean, College Development Council, University of Kashmir, Srinagar.
3. Deans of all Schools, University of Kashmir, Srinagar.
4. Heads of all Teaching Departments, University of Kashmir, Srinagar. The H.O.D Electronics shall also handle start-up of IMSc Electronics with respect to minor with the H.B.U. Computer Science of an earnest.
5. Directors of various Research Centres/Institute/campuses, University of Kashmir, Srinagar.
6. Controller of Examinations, University of Kashmir, Srinagar for kind information & necessary action.
7. Director, Directorate of Admissions & Competitive Examination, University of Kashmir, Srinagar.
8. Spl Secretary to Vice Chancellor for information of the Vice Chancellor.
9. All officers of the Registry, University of Kashmir.
10. P.A to Registrar for information of the Registrar, University of Kashmir.
11. File with 2 separate copies

Joint Registrar
Academic

12-08-2024
12/08/24