

Dr. Javeed Iqbal Reshi

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Curriculum vitae

OBJECTIVE

To be associated with a progressive organization that gives scope to update my practical knowledge and skills in accordance with the latest trends and to be a part of the team that dynamically works towards the growth of the organization and gains satisfaction thereof.

ACADEMIC PERSUITS

- **Ph.D** from Kashmir University (*Realization Digital Structures using Quantum-dot Cellular Automata (QCA)*) (2016).
- **M. Phil** from Kashmir University (*Implementation of QCA for Efficient Digital Structures*) (2012).
- **M.Sc. Electronics** from Kashmir University (Electronics) (1998-2000).
- **B.Sc (with Electronics)** from Kashmir University with (1994-1997).

QUALIFYING EXAMINATIONS

UGC (UNIVERSITY GRANTS COMMISSION) NET 2013(Roll No: 31880001)

TECHNICAL PERSUITS

- Diploma from Islamia College of Science & Commerce.
- **PGDCA** from ICI Delhi in 1991.
- **Diploma in Java, Advanced java & Web Technologies** from SSI New Delhi.
- Diploma in **Internet & Networking** from **TTTI** Chandigarh.

IT PROFILE

Operating System : Window 95, Win 98, Win 2000 professional/ server, Linux.

Languages : C, C++, Java 2.0.

Database : Sql Server 7.0/2000, MySql, Ms Access.

Internet Technologies : HTML, DHTML, JavaScript, VBScript,
Server Side Technology : JSP,ASP.
Simulators : Proteus, Packet Tracer, MultiSim, QCADesigner, Keil.

JOURNAL PAPERS

1. **Javeed Iqbal**, M. Tariq Bandy, “**Realization of Multiplexer as Universal Structure using Quantum dot Cellular Automata**”, *The Mediterranean Journal of Electronics and Communication*, vol. 12, No. 1, 2016. ISSN:1744:2400
2. **Javeed Iqbal Reshi**, M. Tariq Bandy, “**Efficient Design of Reversible Code Converters Using Quantum dot Cellular Automata**”, *Journal of Nano-and Electronic Physics*, Vol. 8 pp.02042 (1-8), 2016. ISSN: 2306-4277
3. **J. I. Reshi**, M. Tariq Bandy, “**Realization of Peres Gate as Universal Structure using Quantum dot Cellular Automata**”, *Journal of Nanoscience and Technology*, Vol. 2, pp. 115-118, 2016, ISSN: 2455-0191.
4. **Javeed Iqbal**, M. Tariq Bandy, “ **Application of Toffoli Gate for Designing the Classical Gates Using Quantum dot Cellular Automata**”, *Internal Journal of Recent Scientific Research*, Vol. 6, pp. 7764-7769, 2016, ISSN:0976-3031
5. N. A. Shah, F. A. Khanday, Z. A. Bangi and **J. Iqbal**, “**Design of Quantum-dot Cellular Automata (QCA) based modular 1 to 2ⁿ Demultiplexers**”, *International Journal of Nanotechnology and Applications*, Vol. 5, No. 1, pp. 47-58, 2011. (ISSN 0973-631X).
6. N. A. Shah, F. A. Khanday and **J. Iqbal**, “**Quantum-dot Cellular Automata (QCA) Design of Multi-Function Reversible Logic Gate**”, *International journal of Communication in Information Science and Management Engineering (CISME) (THE WORLD ACADEMIC PUBLISHING CO., LIMITED, Hong Kong) (Print ISSN:2222-1859 Online ISSN:2224-7785)*, Vol. 2, No. 4, pp. 8-18, 2012.
7. **J.Iqbal**, F. A. Khanday, N. A. Shah “ **Efficient Quantum-dot Cellular Automata (QCA) Implementation of Code Converters**”, *International journal of Communication in Information Science and Management Engineering (CISME) (THE WORLD ACADEMIC PUBLISHING CO.,*

CONFERENCE PAPERS

1. **J. Iqbal**, F. A. Khanday and N. A. Shah, , “**Design of Quantum-dot Cellular Automata (QCA) based modular $2n-1-2n$ MUX-DEMUX**”, Proceedings of the International Conference On Multimedia Signal Processing And Communication Technologies, 978-1-4799-1205-6/13 2013 IEEE, IMPACT 2013, , pp. 255-259, 2013.
2. **Javeed Iqbal Reshi**, M. Tariq Bandy, “**Nano-Scale Design of Reversible Adder Using Quantum dot Cellular Automata**”, 3rd International Conference on Emerging Electronics (ICEE) 2016, IEEE, IIT Mumbai, DOI:10.1109/ICEmElec.2016.8074585.
3. **J. I. Reshi**, M. T. Bandy,” **Efficient Design of Nano-Scale Adder and Subtractor Circuits using Quantum dot Cellular Automata**”, 3rd International Conference on Electrical, Electronics, Engineering Trends, Communication, Optimization and Sciences (EEECOS 2016), IEEE DOI:10.1049/cp.2016.1508.
4. **Javeed Iqbal**, M. Tariq Bandy, “**Sequential Circuit Design using Quantum Dot Cellular Automata (QCA)**”, International Conference on Advances in Computers, Communication and Electronics Engineering (COMMUNE-2015), 16-18 March, University of Kashmir.(*Best paper Presentation*)
5. **J. Iqbal**, N. A. Shah, and F. A. Khanday, “**Efficient reconfigurable logic using Quantum Dot Cellular Automata**”, 8th JK Science Congress, 15-17 September 2012, University of Kashmir.
6. **J. I. Reshi**, N. A. Shah, and F. A. Khanday, “**Quantum Cellular Automaton Digital Logic Design Using And-Or-Inverter (AOI) Gate**”, International Conference on Recent Advances in Electronics and Computer Engineering, 17-18 Dec. 2011, Eternal University, Baru Sahib, HP, India.
7. **J. Iqbal**, N. A. Shah, and F. A. Khanday, “**Design of QCA based N-bit reversible register using reversible computing**”, 7th-JK Science Congress, 13-15 October 2011, University of Jammu.
8. **J.Iqbal**, F. A. Khanday and N. A. Shah, “**Novel Design of Encoders using Quantum Dot Cellular Automata (QCA)**”, 9th J&K Science Congress, 1st Oct-3rd Oct 2013.

SEMINARS

1. **Javeed Iqbal Reshi**, M. Tariq Banday, “**Reversible Computing: Motivation, Progress and Challenges**”, UGC Sponsored National Seminar on Electronic Devices, Systems and Information Security, SEEDS-2015, March 16-17, University of Kashmir (*Best Presentation Award*)
2. **Javeed Iqbal Reshi**, M. Tariq Banday, “**Nanotechnology in Electronics**”, UGC Sponsored National Seminar on Electronic Devices, Systems and Information Security, SEEDS-2016, March 18-19, University of Kashmir.
3. **Javeed Iqbal Reshi**, M. Tariq Banday, “**Electronic Skin: The Future Sense Technology**”, UGC Sponsored National Seminar on Electronic Devices, Systems and Information Security, SEEDS-2017, March 24-25, University of Kashmir.

WORKSHOPS

4. Attended workshop “Global Initiative for Academic Networks” (GIAN) for the course “**Logic Design under Paradigm of Rebooting Computing**”, 25-29 December, 2017, Indian Institute of Technology Roorkee.

PROJECT EXPERIENCE

Software Projects:

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|-----------|--|
| ✓ Project | Integrated Library Management System. |
| Front End | Java |
| Back End | Sql Server 7.0. |
| ✓ Project | Developed the Website for Kashmir University |
| Front End | FrontPage. |
| Back End | MsAccess. |
| ✓ Project | Game Project using c with graphics. |
| Front End | C Compiler. |

Hardware Projects:

- ✓ Installed and Configured the LAN and EDUSAT Network in Govt.Degree College Boys Baramulla.
- ✓ Configured the Wi-Fi Network in Govt.College for Women M.A.Road Srinagar.
- ✓ Maintenance of Edusat Sites in the Kashmir Valley.

SUBJECT OF INTREST

1. **Digital Electronics.**
2. **Computer Arch. & Organization.**
3. **Computer Networking.**
4. **Embedded Systems.**
5. **C++.**
6. **Power Electronics**
7. **Hardware Description Language(VHDL)**

EXPERIENCE

- ✓ Worked as lecturer in **Govt. Degree College Baramulla** (P.G. Centre) 2003-2005.
- ✓ Worked as lecturer in **Govt.College for Women M. A. Road Srinagar** 2006-2009
- ✓ Working as Assistant Professor (on Contract) in the Department of Electronics & Instrumentation Technology, Kashmir University from 2009 till date.
- ✓ Working as academic Councilor/Project Evaluator at IGNOU Regional Centre, Srinagar.

PERSON DETAILS

Name:	Dr.Javeed Iqbal Reshi.
Father's Name	Mr. Bashir Ahmad Reshi.
D.O.B	01-01-1975
Martial Status	Married.
Permanent Address	Ellahibagh, Buchpora. Srinagar.
Correspondence Address	Deptt. of Electronics & IT University of Kashmir

I hereby assure that the Information given above is thoroughly correct to the best of my knowledge.

Dated: 23/06/2017

Place: Srinagar.

Signature